

FIG. 1

Express Mail No. EV301186313US
Application No. Continuation of REI 09/972,847
Title: SYSTEM FOR DATA TRANSFER THROUGH AN I/O DEVICE
USING A MEMORY ACCESS CONTROLLER WHICH RECEIVES
AND STORES INDICATION OF A DATA STATUS SIGNAL
Inventor: Kevin M. Christiansen
Atty. Docket No.: 18602-08301
Sheet 2 of 7

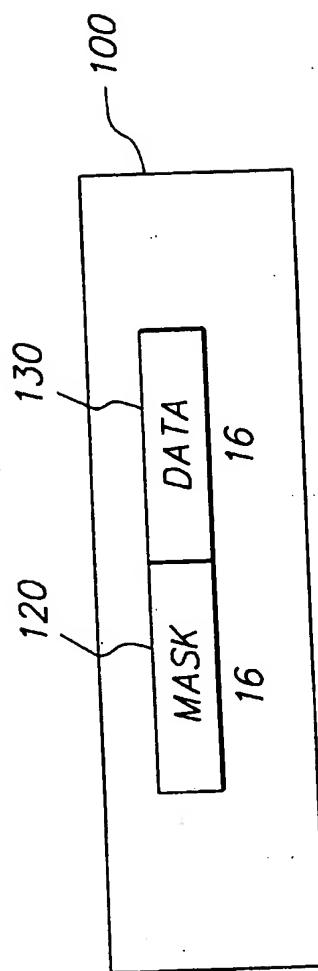


FIG. 2

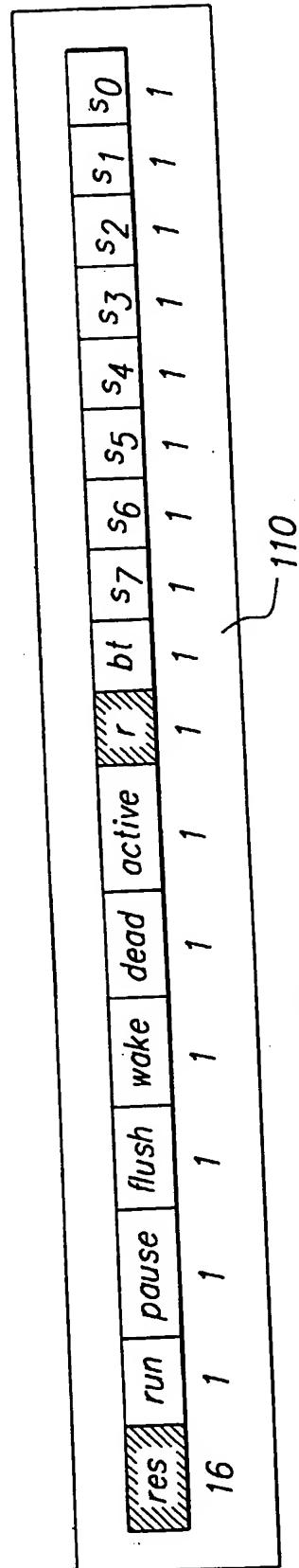


FIG. 3

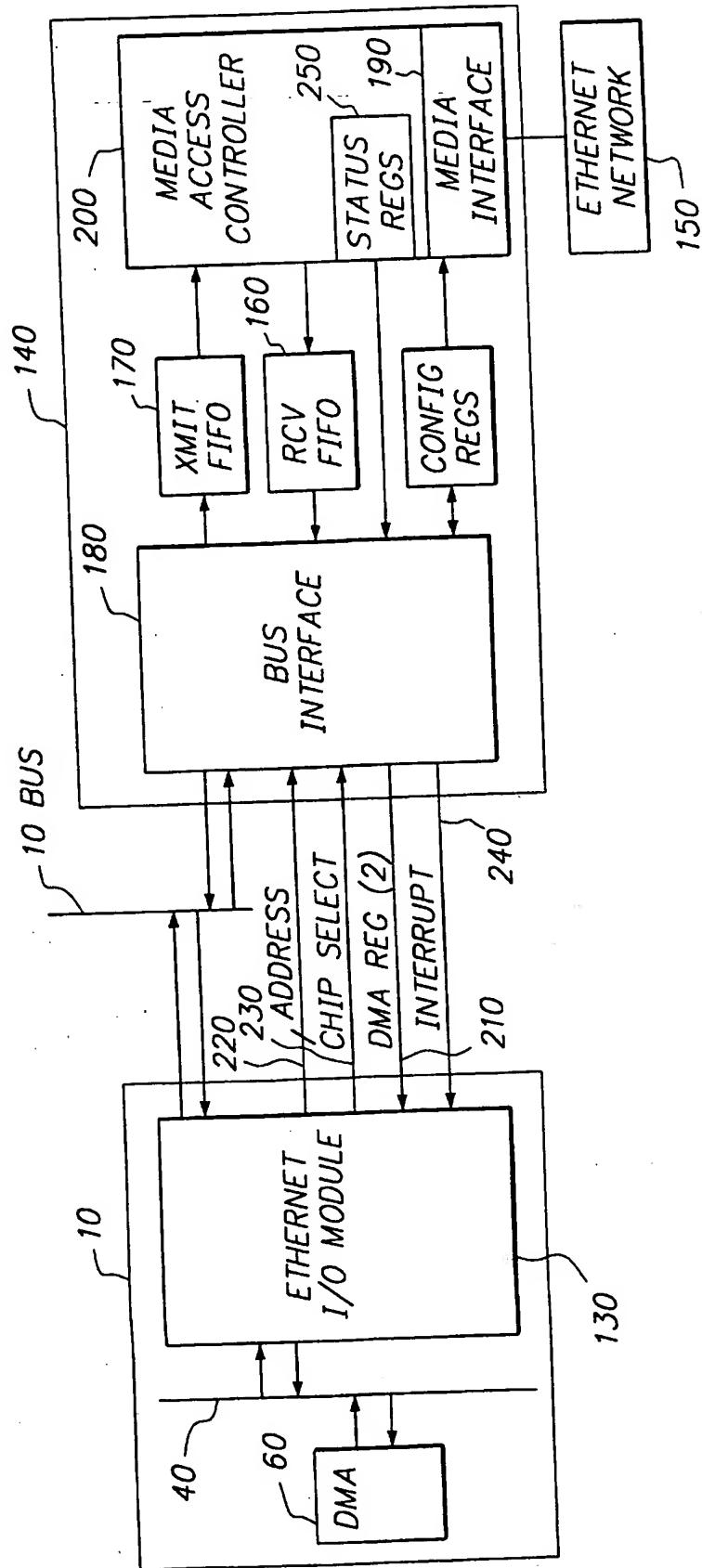


FIG. 4

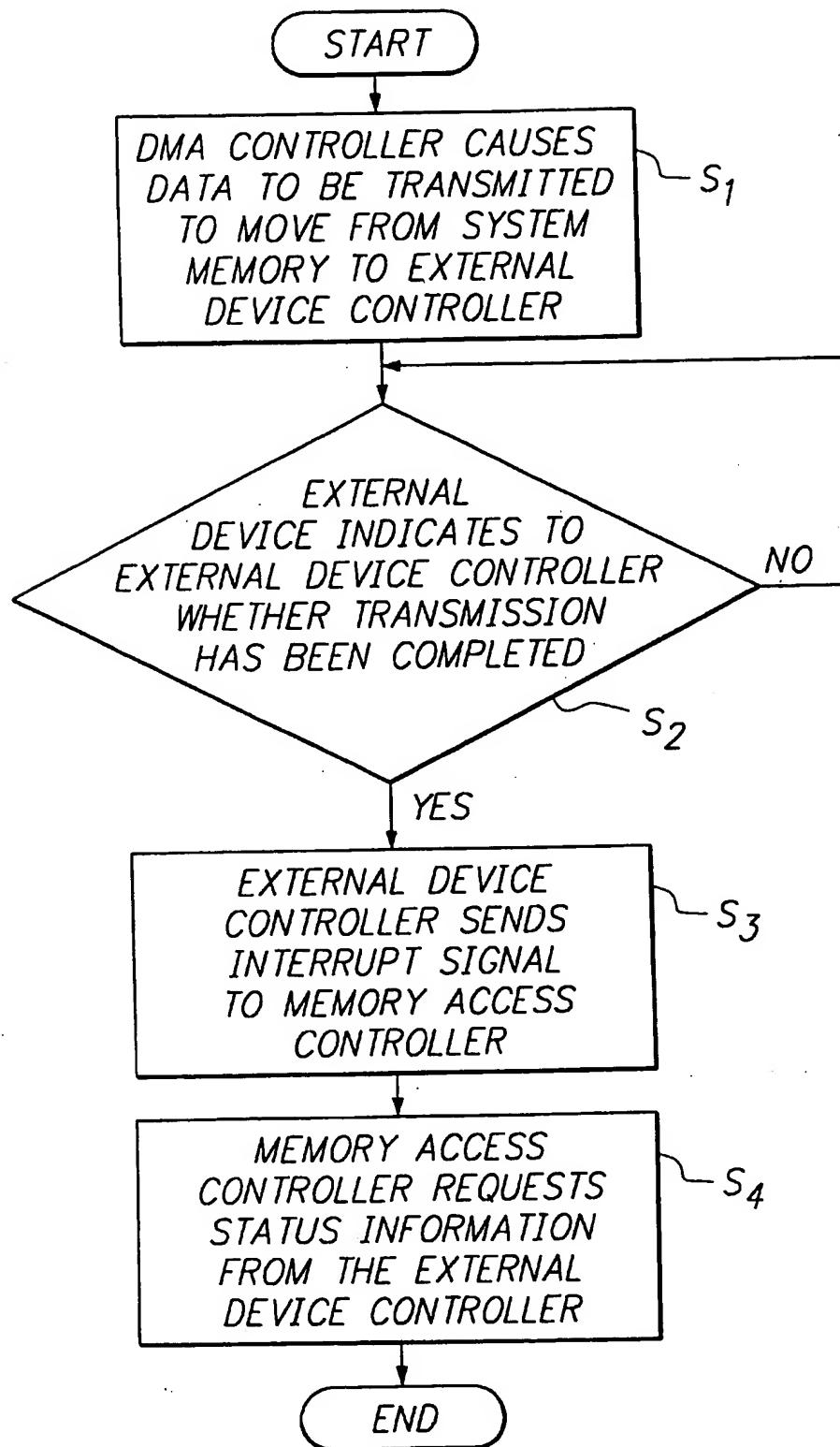


FIG. 5

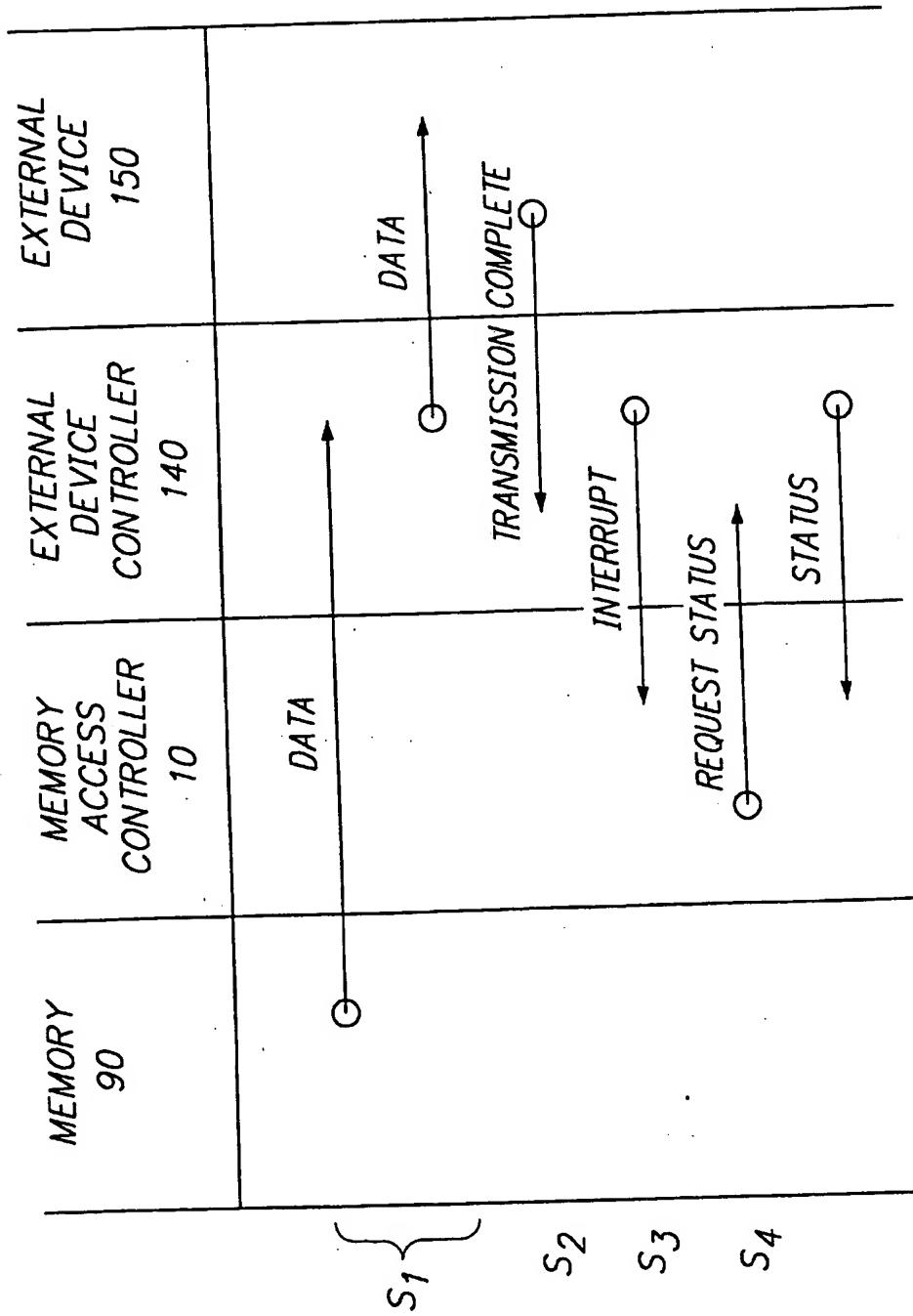


FIG. 6

BIT	MEANING
S_7	ERROR
S_6	NOT IMPLEMENTED
S_5	MACE INTERRUPT
$S_4 \dots S_1$	NOT IMPLEMENTED
S_0	WAIT (EXTERNALLY CONTROLLED)

FIG. 7

BIT	MEANING
S_7	NOT IMPLEMENTED
S_6	EOP (END OF PACKET)
$S_5 \dots S_1$	NOT IMPLEMENTED
S_0	WAIT (EXTERNALLY CONTROLLED)

FIG. 9

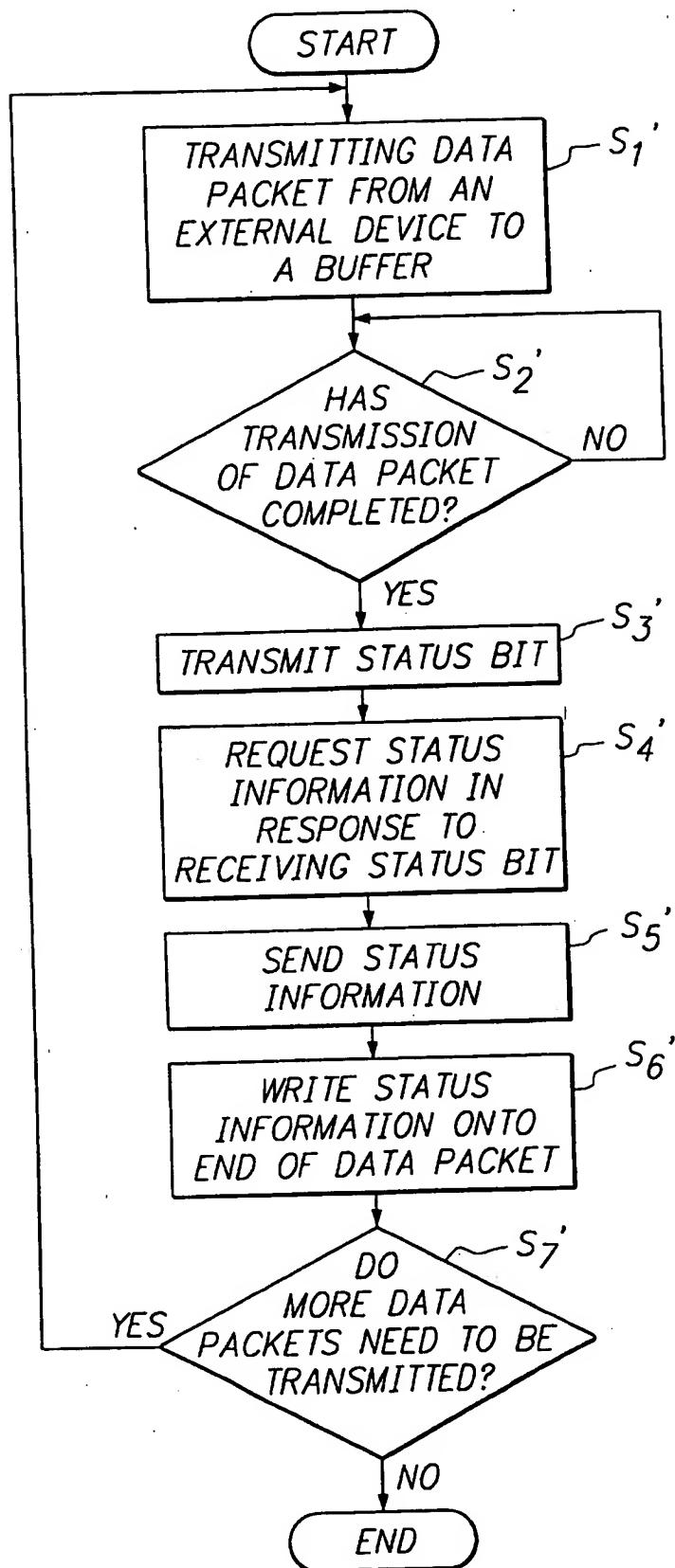


FIG. 8